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Analog means continuous and real. The world we live in is analog in nature, implying that it's full of infinite possibilities. The number of smells we can sense, the number of tones we can hear, or the number of colors we can paint with; everything is infinite.

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An introductory course on analog and digital communications is fundamental to the under-graduate program in electrical engineering. This course is usually offered at the junior level. Typically, it is assumed that the student

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has a background in calculus, electronics, signals and systems, and possibly probability theory.

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The last section of this article deals with some of the issues to be aware of when using such ADCs. But first we'll review the basics of Analog-to-Digital conversion. Basics of Analog to Digital Conversion. An ADC is represented by the schematic symbol in figure 1. It just

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The process of converting analog information into digital data is called "Sampling". The process of converting digital data into an analog signal is called "Reconstruction". We will talk about both processes in a later chapter. For more information on the topic than is available in this book, see the Analog and Digital Conversion wikibook. Here ...

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Signals and Systems was developed in

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1987 as a distance-education course for engineers. An introduction to analog and digital signal processing, including discrete- and continuous-time signals, linear time-invariant systems, feedback, and data processing. Signals and Systems: an Introduction to Analog and Digital Signal Processing, 1987 MIT

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This article provides a brief description of the SPI interface followed by an introduction to Analog Devices' SPI enabled switches and muxes, and how they help reduce the number of digital GPIOs in system board design. SPI is a synchronous, full duplex master-slave-based interface. The data from the master or the slave is synchronized on the ...

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using components available in the ADALP2000 Analog Parts Kit. The cutoff frequency of the lowpass filter is designed to be approximately 100 Hz, and the highpass filter cutoff frequency is designed to be approximately 200 Hz.

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AN-2003: On-Chip Oversampling for the Analog Devices, Inc., AD7380 Family of SAR ADCs. by Jonathan Colao Download PDF. INTRODUCTION. This application note discusses on-chip oversampling in successive approximation register (SAR), analog-to-digital converters (ADCs). Two common oversampling techniques are normal average and rolling average.

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